

**FIG. 1**

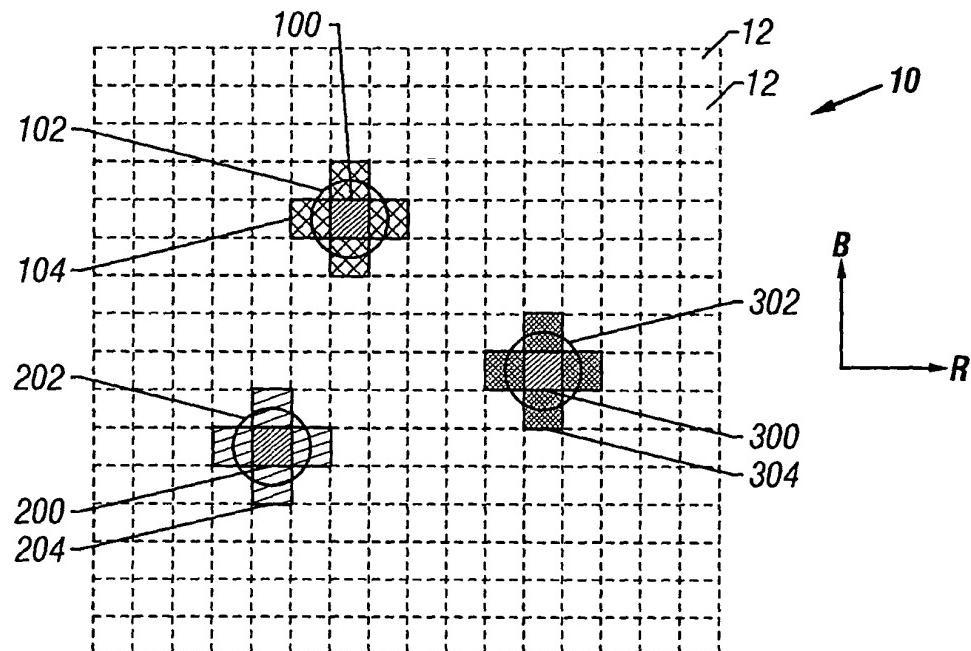


FIG. 2A

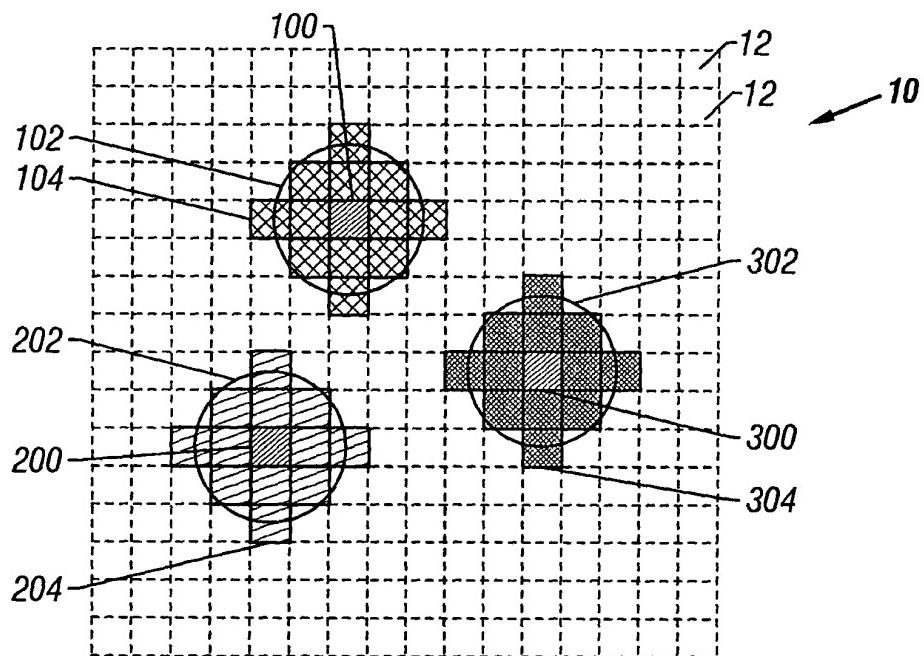


FIG. 2B

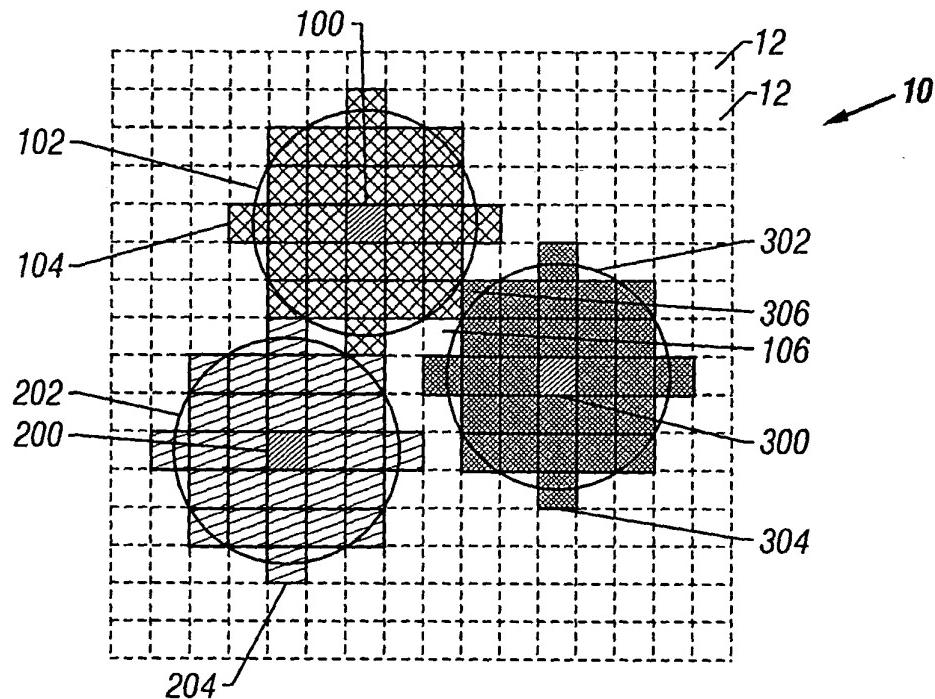


FIG. 2C

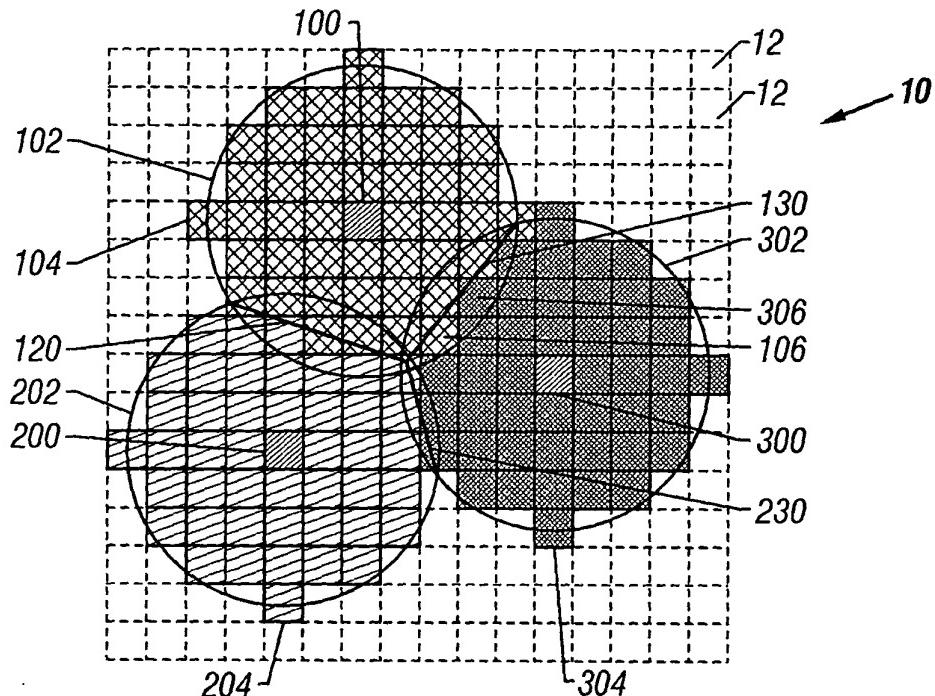


FIG. 2D

4/7

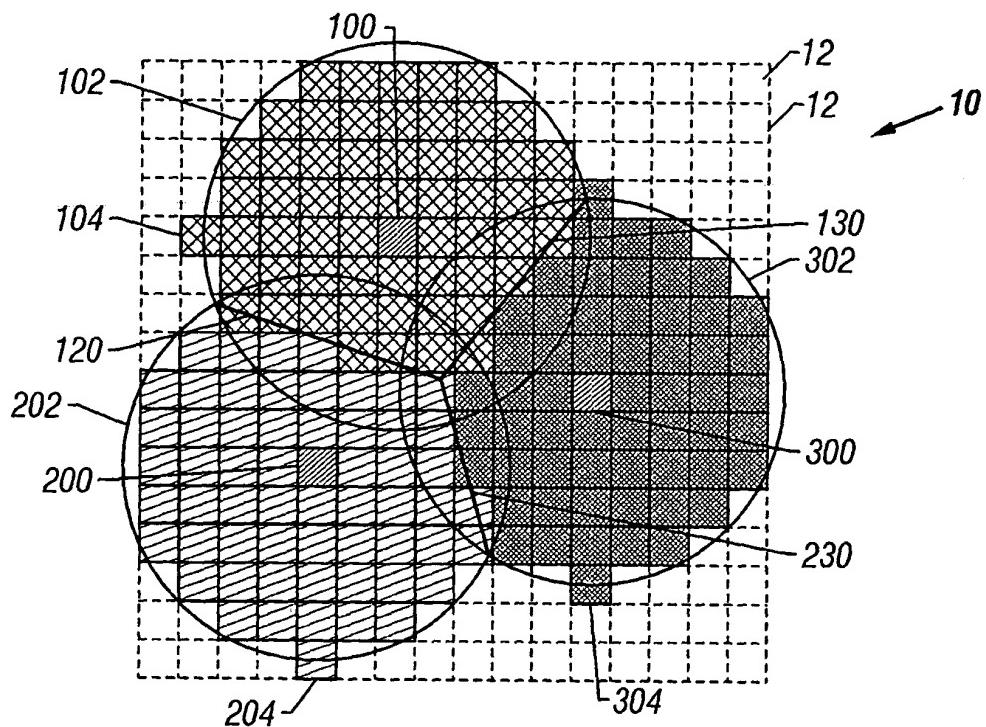


FIG. 2E

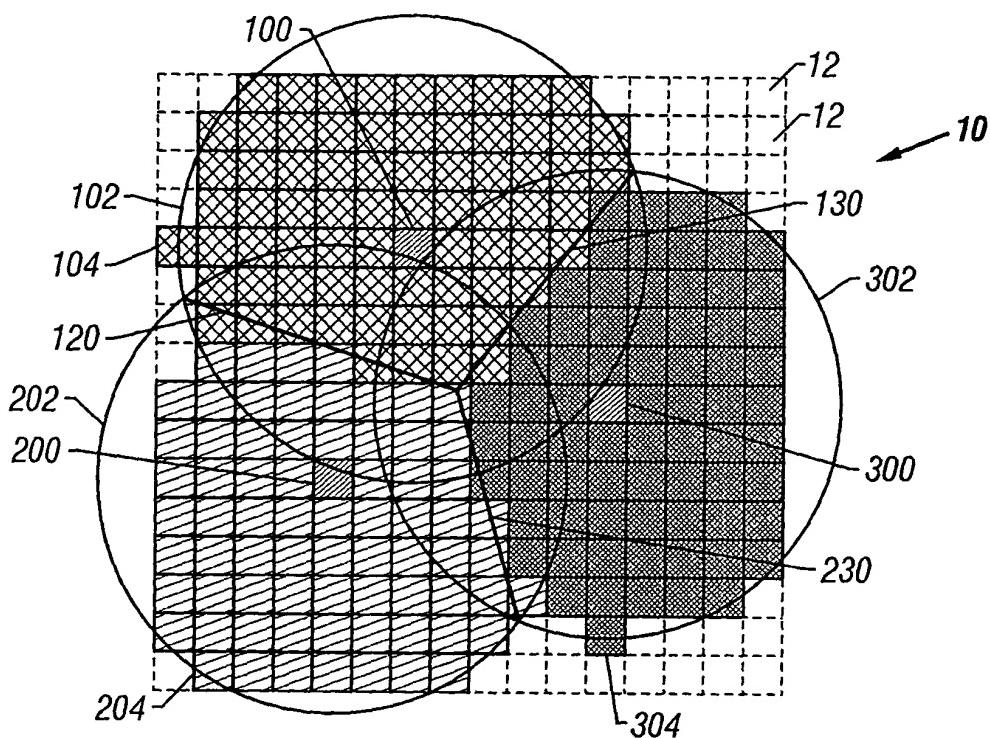


FIG. 2F

<i>Discrete Location</i>	<i>Mapping Value (Upper byte)</i>	<i>Bit Flags (Lower byte)</i>
(0, 0, 0)		
(0, 0, 1)		
(0, 0, 2)		
.		
(0, 0, 255)		
(0, 1, 0)		
(0, 1, 1)		
(0, 1, 2)		
.		
(0, 1, 255)		
(0, 2, 0)		
(0, 2, 1)		
(0, 2, 2)		
.		
(0, 2, 255)		
.		
(255, 255, 255)		

FIG. 3A

<i>Mapping Value</i>	<i>Display Color</i>
0	
1	
2	
3	
.	
255	

FIG. 3B

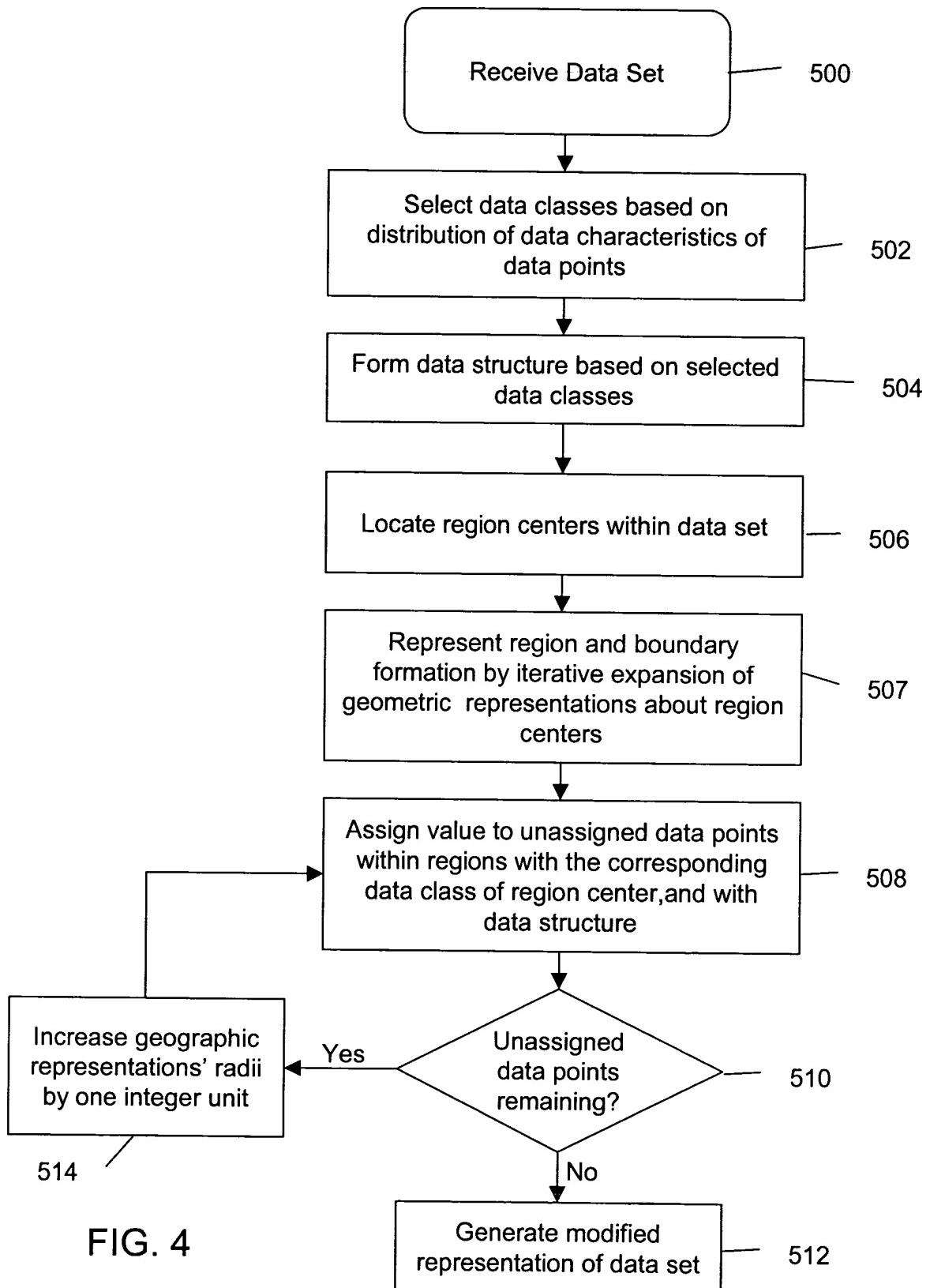


FIG. 4

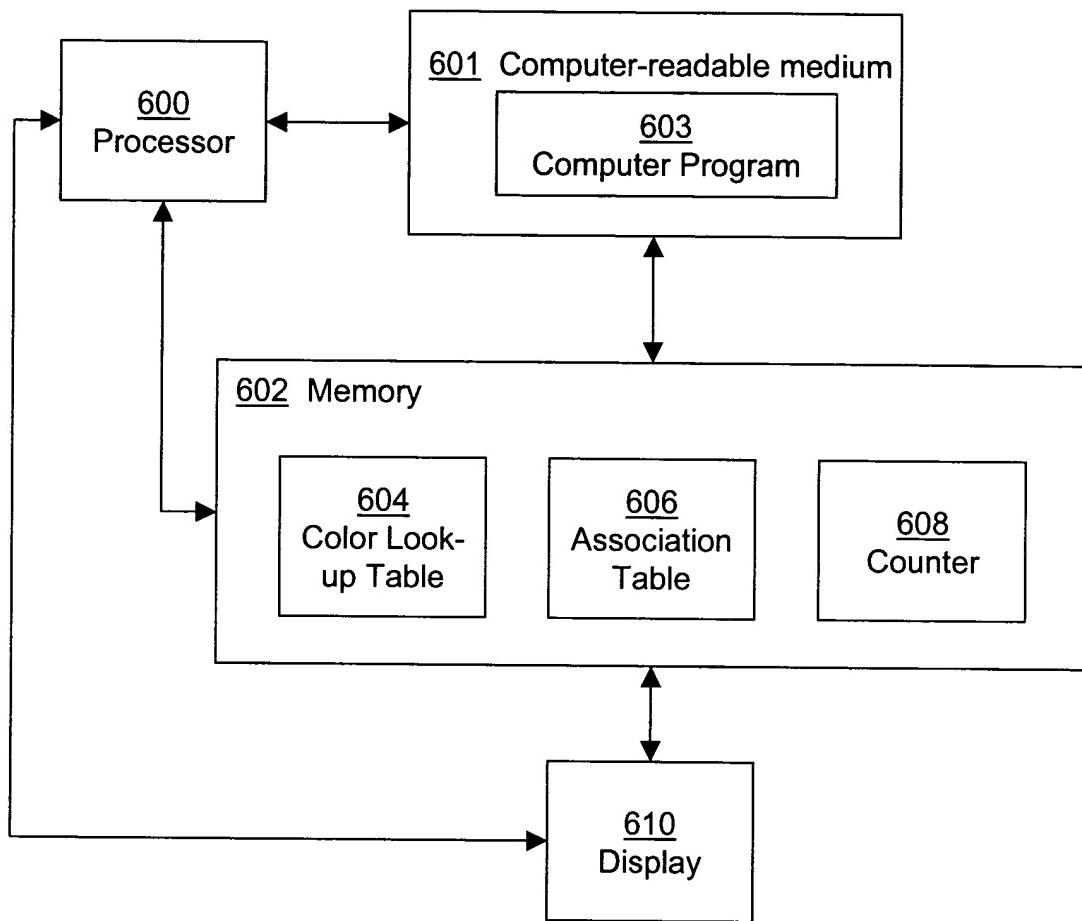


FIG. 5